

U.S. Patent Application No. 09/870,296
Reply to Office Action dated August 10, 2005

PATENT
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IN THE SPECIFICATION

Please amend as follows:

On page 11:

In the receiving station 100, the CPU (Central Processing Unit) 11 as a main controller connects with each hardware component via a bus 50 for centrally controlling these components. The following describes each component of the content recording and reproducing system 40 receiving station 100.

On page 12:

The decoder 53 analyzes this transport stream and separates it into MPEG2 compressed AV data and data broadcasting data. The decoder further separates MPEG2-compressed realtime AV data into compressed video data and compressed audio data. The decoder reproduces the original video signal from the video data by means of MPEG2 expansion. The decoder decodes the audio data according to PCM (Pulse Code Modulation) and synthesizes this data with an additional sound to generate a reproduction audio signal. The decoder 53 may be locally equipped with memory 54 as a work data storage. The reproduced video signal is displayed on a display 61 via a composer 57. The reproduced audio signal is output to a speaker 62 via a ~~mixer 55~~ MUX 55A. For implementing the present invention, audio data is not limited to the PCM format as mentioned above, but may use other formats such as MPEG AUDIO, Real Audio, and Quick Time.

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On page 13:

The CPU (Central Processing Unit) 11 is a main controller for managing operations of the entire ~~receiving station 10~~ receiving station 100 and can execute various applications on a platform provided from the operating system (OS).

RAM (Random Access Memory) 12 is write-enabled volatile memory used for loading executable program codes for the CPU 11 and writing work data for an executable program. ROM (Read-Only Memory) 13 is write-protected memory for permanently storing control codes for hardware operations and self-diagnostic or initialization programs executed during a power-on sequence for the content recording and ~~reproducing system 10~~ receiving station 100.

On pages 13-14:

An IEEE1394 (i-link) interface 15 is a high-speed serial interface capable of sending and receiving data at a rate of several tens of megabytes per second. An IEEE1394 port can be used for connection with IEEE1394-compliant external devices in a daisy-chain or tree fashion. IEEE1394-compliant devices include, say, a video deck 64 for recording or reserving broadcast contents.

On page 14:

A hard disc drive (HDD) 17 is a random-access external storage capable of storing programs and data in specified file formats and provides a large capacity of, say, several tens of gigabytes (or 100 GB or more). The HDD 17 is connected to the bus 50 via a ~~hard disc interface 18~~ hard disc interface 16. A request from the CPU 11 allows broadcast programs to be saved or recorded in an MPEG2 stream format on the HDD 17.